		— ,		
-	785	713/193.ccls.	USPAT; US-PGPUB;	2004/08/08 15:19
			EPO; JPO; DERWENT;	
	400	207/545	IBM_TDB	0004400400 15 15
-	428	327/545.ccls.	USPAT; US-PGPUB;	2004/08/08 15:19
f			EPO; JPO; DERWENT;	
	021	711 (1621-	IBM_TDB	0004/00/00 15 07
_	931	711/163.ccls.	USPAT; US-PGPUB;	2004/08/08 15:27
			EPO; JPO; DERWENT;	
_	9961	(FPGA "Field Programable gate array"	IBM TDB USPAT;	2004/08/08 18:05
	9901	FPGAs "Field Programable gate arrays")	US-PGPUB;	2004/00/00 10:03
		and ((integrated adj circuits) IC)	EPO; JPO; DERWENT;	
_	173386	(key (mask)) and (\$2crypt\$3 \$2cipher\$3	<pre>IBM_TDB USPAT;</pre>	2004/08/08 18:05
		\$2compress\$3 \$2scrambl\$3)	US-PGPUB; EPO; JPO;	
			DERWENT;	
_	664	((FPGA "Field Programable gate array"	IBM_TDB USPAT;	2004/08/08 15:38
		FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and	US-PGPUB; EPO; JPO;	
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	DERWENT; IBM TDB	
		"cyclic Redundancy Check" checksum MAC	IBM_IDB	
_	6640475	<pre>"message authentication code" signature) (first one primary 1st) and (second\$3 two</pre>	USPAT;	2004/08/08 18:06
		2nd alternative)	US-PGPUB; EPO; JPO;	
			DERWENT; IBM TDB	
-	4	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays")	USPAT; US-PGPUB;	2004/08/08 15:45
		and ((integrated adj circuits) IC)) and	EPO; JPO;	
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	DERWENT; IBM_TDB	
		"cyclic Redundancy Check" checksum MAC "message authentication code" signature)		
		and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd		
	138	alternative))) and 711/163.ccls. ((((FPGA "Field Programable gate array"	HCDATT.	2004/08/08 15:45
	130	FPGAs "Field Programable gate arrays")	USPAT; US-PGPUB;	2004/08/08 13:45
-		and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3	EPO; JPO; DERWENT;	
		\$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC	IBM_TDB	
		"message authentication code" signature) and (pseudo random)) and ((first one		
		primary 1st) and (second\$3 two 2nd		·
_	0	alternative))) and "111" ((((FPGA "Field Programable gate array"	USPAT;	2004/08/08 15:45
		FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and	US-PGPUB; EPO; JPO;	
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	DERWENT; IBM TDB	
		"cyclic Redundancy Check" checksum MAC "message authentication code" signature)	_	
		and (pseudo random)) and ((first one		
		primary 1st) and (second\$3 two 2nd alternative))) and 327/545.ccls.		

-	6	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays")	USPAT; US-PGPUB;	2004/08/08 15:48
		and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	EPO; JPO; DERWENT; IBM TDB	
		"cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one	_	
		primary 1st) and (second\$3 two 2nd alternative))) and 713/193.ccls.		
-	1074	713/2.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/08 15:49
-	787	713/100.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:49
_	0	713/1190.ccls.	DERWENT; IBM_TDB USPAT;	2004/08/08 15:49
	Ů		US-PGPUB; EPO; JPO; DERWENT;	
_	160	713/190.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:49
_	· 785	713/193.ccls.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:49
_	239	713/194.ccls.	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/08 15:49
-	171	713/187.ccls.	EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/08/08 15:49
	171	7137107.0013.	US-PGPUB; EPO; JPO; DERWENT;	2004/00/00 13.49
_	3	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:50
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd	DERWENT; IBM_TDB	
-	2	FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and	USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:52
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature)	DERWENT; IBM_TDB	
		and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/100.ccls.		

-	0	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:52
-	1	and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/1190.ccls. ((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:52
_	6	"cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/187.ccls. ((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and	USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:55
-	5	((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/190.ccls. ((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays")	DERWENT; IBM_TDB USPAT; USPAT; US-PGPUB;	2004/08/08 15:53
		and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/194.ccls.	EPO; JPO; DERWENT; IBM_TDB	
_	6	(((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:55
-	484	alternative))) and 713/193.ccls. (((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:56
-	7	alternative))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:06
-	4	((FPGA with chip) and (memory storage "hard drive") and ("Field Programable Interconnect Components" FPICs)) and ((\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:05

_	127	(FPGA with chip) and (memory storage "hard drive") and (((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:43
-	2	09/780681	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:39
-	1	(FPGA with chip) and (contact adj layer) and (polysilicon adj wire)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 18:07
_	179	(polysilicon adj wire)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:43
-	1	(polysilicon adj wire) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:45
_	489	(polysilicon) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:45
_	1165	(SRAM "Static Random Access Memory") with FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:57
-	79	((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and ((SRAM "Static Random Access Memory") with FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:57
_	965	"53" and CBC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 17:19
_	2	("6356637").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 17:36
_	8	(("6324286") or ("4878246") or ("6615349") or ("5036468")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 17:38
-	2	("6357037") PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 18:05

		•		
<u> </u>	2	(FPGA "Field Programable gate array"	USPAT;	2004/08/08 18:07
		FPGAs "Field Programable gate arrays"	US-PGPUB;	1
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scramb1\$3 CRC "cyclic Redundancy Check"	IBM_TDB	
		checksum MAC "message authentication code"		
		signature first one primary 1st second\$3		
		two 2nd alternative) and (("6357037").PN.)		
_	2		USPAT;	2004/08/09 04:49
		FPGAs "Field Programable gate arrays"	US-PGPUB;	
		(integrated adj circuits) IC key mask	EPO; JPO; DERWENT;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	
		checksum MAC "message authentication code"	1514-108	
		signature first one primary 1st second\$3		
		two 2nd alternative FPGA chip contact		
		layer polysilicon adj wire) and		
		(("6357037").PN.)		
_	1262		USPAT;	2004/08/08 18:12
	1202	Dulimam-y-y.in.	US-PGPUB;	2004/08/08 18:12
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	18	burnham-\$-\$.in. and FPGA	USPAT;	2004/08/08 18:12
		Darman y y.in. and iron	US-PGPUB;	2004,00,00 10.12
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	18	(FPGA "Field Programable gate array"	USPAT;	2004/08/08 23:33
		FPGAs "Field Programable gate arrays"	US-PGPUB;	2001,00,00 23:33
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scramb1\$3 CRC "cyclic Redundancy Check"	IBM TDB	
		checksum MAC "message authentication code"		
		signature first one primary 1st second\$3		1
		two 2nd alternative FPGA chip contact		
		layer polysilicon adj wire) and		1
		(burnham-\$-\$.in. and FPGA)		
_	0	(FPGA "Field Programable gate array"	USPAT;	2004/08/08 18:29
		FPGAs "Field Programable gate arrays"	US-PGPUB;	
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	
		checksum MAC "message authentication code"		
		signature first one primary 1st second\$3		
		two 2nd alternative FPGA chip contact		
		layer polysilicon adj wire) and		
		(burnham-\$-\$.in. and FPGA) and mask		
_	78	713/100.ccls. and mask	USPAT;	2004/08/08 18:29
			US-PGPUB;	}
			EPO; JPO;	
			DERWENT;	
		/ / ma	IBM_TDB	0004/00/00
-	19	1 ,,	USPAT;	2004/08/08 18:29
		US-6324676-\$ or US-6640305-\$ or	US-PGPUB;	
		US-6633905-\$ or US-6408432-\$ or	EPO; JPO;	
		US-6324288-\$ or US-6279045-\$ or	DERWENT;	
		US-6215327-\$ or US-6151677-\$ or	IBM_TDB	
		US-6147890-\$ or US-6134707-\$ or		
		US-5717394-\$ or US-5687325-\$ or		
		US-6772387-\$ or US-6560743-\$ or US-6357037-\$ or US-6118869-\$ or		
		US-6049222-\$).did. or (US-20040111631-\$ or US-20020099948-\$ or US-20020026539-\$ or		
		US-20020099948-\$ or US-20020026339-\$ or US-20010032318-\$ or US-20010015919-\$ or		
		US-20010032318-\$ of US-20010013919-\$ of US-20010037482-\$).did.) and mask		l
	!	1 00 2001000 1402 VI. ala. J alla mask	L	L

0	713.200.ccls.	USPAT;	2004/08/09 00:20
		EPO; JPO; DERWENT;	
0	380.200.ccls.	USPAT; US-PGPUB;	2004/08/09 00:20
		EPO; JPO; DERWENT; IBM TDB	
0	380.30.ccls.	USPAT; US-PGPUB; EPO; JPO;	2004/08/09 00:21
53	714/100.ccls.	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/09 00:22
371	717/124.ccls.	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/09 00:23
180	717/139.ccls.	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/09 00:22
6	717/139.ccls. and FPGA	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/09 00:22
10	Kelem-\$-\$.in. and FPGA	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/09 01:02
48	(xilinx same FPGA) and mask and (distribut\$3 provider) and multiple and	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/09 01:16
5.6	\$2compress\$3 \$2scrambl\$3)	DERWENT; IBM_TDB	2004/08/09 01:18
30	and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)	US-PGPUB; EPO; JPO; DERWENT;	2004/00/03 01:18
8	((xilinx same FPGA) and mask and multiple and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) not ((xilinx same FPGA) and mask and (distribut\$3 provider) and multiple and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:16
56	\$2scramb1\$3)) (xilinx same FPGA) and mask and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scramb1\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/09 01:18
132	<pre>(xilinx same FPGA) and mask and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/09 01:18
76	(\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) not ((xilinx same FPGA) and mask and bitstream and (\$2crypt\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/09 01:19
	0 0 53 371 180 6 10 48 56	0 380.200.ccls. 0 380.30.ccls. 53 714/100.ccls. 53 717/124.ccls. 6 717/139.ccls. and FPGA 10 Kelem-\$-\$.in. and FPGA 10 Kelem-\$-\$.in. and FPGA 48 (xilinx same FPGA) and mask and (distribut\$3 provider) and multiple and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2compress\$3 \$2comptes\$3 \$2cipher\$3 \$2compress\$3 \$2comptes\$3 \$2cipher\$3 \$2compress\$3 \$2comptes\$3 \$2compte	0 380.200.ccls.

_	2	(FPGA "Field Programable gate array"	USPAT;	2004/08/09 03:50
		FPGAs "Field Programable gate arrays"	US-PGPUB;	
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	
		checksum MAC "message authentication code"	_	
		signature first one primary 1st second\$3		
		two 2nd alternative FPGA chip contact	1	
		layer polysilicon adj wire) and		
		(("6061449").PN.)		